

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of )  
Soichi KOBAYASHI et al. ) Group Art Unit: Unassigned  
Application No.: Unassigned ) Examiner: Unassigned  
Filed: August 4, 2003 ) Confirmation No.: Unassigned  
For: SEMICONDUCTOR INTEGRATED )  
CIRCUIT CAPABLE OF TESTING )  
WITH SMALL SCALE CIRCUIT )  
CONFIGURATION )

**FIRST  
INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In accordance with the duty of disclosure as set forth in 37 C.F.R. § 1.56, Applicants hereby submit the following information in conformance with 37 C.F.R. §§ 1.97 and 1.98. Pursuant to 37 C.F.R. § 1.98, a copy of each of the documents cited is enclosed.

**JP 1-224998 is discussed in the specification.**

The documents are being submitted within three (3) months of the filing or entry of the national stage of this application or before the first Office Action on the merits, whichever is later. Since documents are being filed within the time period set forth in 37 C.F.R. § 1.97(b) no fee or statement is required.

To assist the Examiner, the documents are listed on the attached form PTO-1449. It is respectfully requested that an Examiner-initialed copy of this form be returned to the undersigned.

Respectfully submitted,

BURNS, DOANE, SWECKER & MATHIS, L.L.P.

Date: August 4, 2003  
By:

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Substitute for forms 1449A/PTO & 1449B/PTO	ATTORNEY'S DKT NO. 009683-476	APPLICATION NO. Unassigned
<b>FIRST INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>	APPLICANT Soichi KOBAYASHI et al.	
	FILING DATE August 4, 2003	GROUP Unassigned

FOREIGN PATENT DOCUMENTS					
Examiner Initials	Document Number	Kind Code (if known)	Country	Date of Publication (MM-DD-YYYY)	Translation Yes No
	2000-339228	A	Japan	12-08-2000	abst
*	1-224998	A	Japan	09-07-1989	abst

Examiner Signature		Date Considered	
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with M.P.E.P. § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. **SEND TO:** Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

\*JP 1-224998 is discussed in the specification.